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Application No. 10/823,572 Conf. No. 4816		Filing Date April 14, 2004		Examine T.Q. Pha		
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plicant(s): Kou	Nagata, et al.					
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Benald R Kana	inen			Dated:	August 19, 2005	
Attorney Reg. N						
RADER, FISHM		R PLLC				
12/33 20th Stree Suite 501	et, N.W.					
Washington, DC	20036					
(202) 955-3750						



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Patent Application of:

Kou Nagata, et al.

Art Unit: 2827

Application No.: 10/823,572

Conf. No. 4816

Filed: April 14, 2004

Examiner: T.Q. Phan

For: SEMICONDUCTOR MEMORY APPARATUS

AND SELF-REPAIR METHOD

AFTER FINAL RESPONSE AND REQUEST FOR RECONSIDERATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In full and timely response to the Final Office Action dated June 21, 2005, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.